



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Chainer et al.  
Serial No.: 09/723,362 Group Art Unit: 2651  
Filed: 11/27/00 Examiner: A. Sniezek  
Title: METHOD AND APPARATUS FOR CORRECTING FOR SYSTEMATIC  
ERRORS IN TIMING PATTERN GENERATION

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I hereby certify that this correspondence is being deposited with the U.S. Postal Service as first class mail in an envelope addressed to: Assistant Commissioner for Patents, Washington, D.C. 20231, on August 23, 2001.

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Date of Signature: August 23, 2001

Assistant Commissioner for Patents  
Washington, D.C. 20231

Response to Office Action

Dear Sir:

This Response to Office Action is being submitted in reply to the Office Action mailed April 23, 2001 for the above-referenced patent application. A response to the Office Action was initially due on or before July 23, 2001, and thus, a Request For Extension Of Time and the requisite